

L Number	Hits	Search Text	DB	Time stamp
17	15	clock same buffer and slack same predetermined and latch	USPAT; US-PGPUB; EPO; DERWENT	2004/09/30 17:21
18	10	clock same buffer and slack same predetermined and latch and 716/\$.ccls.	USPAT; US-PGPUB; EPO; DERWENT	2004/09/30 17:21
19	10	clock same buffer and slack same predetermined and latch and (buffer or inverter) and 716/\$.ccls.	USPAT; US-PGPUB; EPO; DERWENT	2004/09/30 18:17
21	2	latch same power same slack and threshold and buffer and timing	USPAT; US-PGPUB; EPO; DERWENT	2004/09/30 18:18
22	95	latch same clock and slack and threshold and buffer and timing	USPAT; US-PGPUB; EPO; DERWENT	2004/09/30 18:19
23	12	latch same clock and slack and threshold and buffer and timing and 716/\$.ccls.	USPAT; US-PGPUB; EPO; DERWENT	2004/09/30 18:24
24	16	latch and power and (clock same buffer) and slack and threshold and timing and 716/\$.ccls.	USPAT; US-PGPUB; EPO; DERWENT	2004/09/30 18:31
25	7	power same load and (clock same buffer) and slack and threshold and timing and 716/\$.ccls.	USPAT; US-PGPUB; EPO; DERWENT	2004/09/30 18:33
26	4	power and (clock same buffer) and latch and (clock same tree) and slack and threshold and timing and 716/\$.ccls.	USPAT; US-PGPUB; EPO; DERWENT	2004/09/30 18:35
27	7	power and (clock same buffer) and latch and (clock same tree) and slack and timing and 716/\$.ccls.	USPAT; US-PGPUB; EPO; DERWENT	2004/09/30 18:37
28	7	power and (clock same buffer) and latch and (clock same tree) and slack and 716/\$.ccls.	USPAT; US-PGPUB; EPO; DERWENT	2004/09/30 18:37
29	7	(power same reduc\$4) and (clock same buffer) and (clock same tree) and slack and 716/\$.ccls.	USPAT; US-PGPUB; EPO; DERWENT	2004/09/30 18:49
30	5	(power same reduc\$4) and (clock same tree) and slack and latch and 716/\$.ccls.	USPAT; US-PGPUB; EPO; DERWENT	2004/09/30 18:54
31	12	(power same reduc\$4) and (clock same buffer) and slack and latch and 716/\$.ccls.	USPAT; US-PGPUB; EPO; DERWENT	2004/09/30 18:55
32	25	(power same reduc\$4) and clock and buffer and slack and latch and 716/\$.ccls.	USPAT; US-PGPUB; EPO; DERWENT	2004/09/30 19:01
33	151	(power same reduc\$4) and (latch or flip-flop) and replace\$4 and drive and 716/\$.ccls.	USPAT; US-PGPUB; EPO; DERWENT	2004/09/30 19:02

34	44	(power same reduc\$4) and (latch or flip-flop) and replace\$4 and drive and skew and 716/\$.ccls.	USPAT; US-PGPUB; EPO; DERWENT	2004/09/30 19:10
36	270	(power same reduc\$4) and (latch or flip-flop or element) and (replace\$4 or substitut\$4) and drive and (slack or delay) and 716/\$.ccls.	USPAT; US-PGPUB; EPO; DERWENT	2004/09/30 19:11
35	61	(power same reduc\$4) and (latch or flip-flop or element) and (replace\$4 or substitut\$4) and drive and slack and 716/\$.ccls.	USPAT; US-PGPUB; EPO; DERWENT	2004/09/30 19:29
37	1	minami.in. and (latch or flip-flop or element) and (replace\$4 or substitut\$4) and drive and slack and 716/\$.ccls.	USPAT; US-PGPUB; EPO; DERWENT	2004/09/30 19:30
38	12	minami.in. and 716/\$.ccls.	USPAT; US-PGPUB; EPO; DERWENT	2004/09/30 19:30